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PATENT APPLICATION

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Inventor(s): Theodore C Briggs et al
Application No.: 10/756448
Filing Date: Jan 12, 2004

Confirmation No.: 2130
Examiner: Unknown
Group Art Unit:

FEB 27 2006Title: **Memory Controller Connection To RAM Using Buffer Interface**

Commissioner For Patents
PO Box 1450
Alexandria, VA 22313-1450

STATUS INQUIRYMore than 24 months have passed since: NEW APPLICATIONSThe filing of this application on Jan 12, 2004. No communication has been received from the Patent and Trademark Office indicating action on this application. AMENDED APPLICATIONSThe filing of a response on . No communication has been received from the Patent and Trademark Office. APPEALED APPLICATIONThe Appeal Brief was filed on . An Examiner's Answer was mailed on . A Reply Brief to the Examiner's Answer was submitted on . ALLOWED APPLICATIONSThe mailing of FORM POL-85 and/or Examiner's Amendment on .

Kindly advise the undersigned of the present status of this application.

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Respectfully submitted,

By

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